AMENDMENTS TO THE CLAIMS

1. (Currently Amended) A method of accessing PCI bus data via a debug card, comprising:

accessing to data of the PCI bus via a PCI interface of the debug card;

storing the data in a buffer of the debug card;

controlling the access to the data stored in the buffer of debug card by means of a data control chip of the debug card, the step of controlling the access to the data stored in the buffer of debug card comprising:

initializing the data control chip;

if the data control chip is in an idle status, setting the data control chip, wherein the setting step further comprises:

setting a data access mode of the data control chip;

determining a data access situation of the debug card and performing counting;

setting an amount of data to be accessed each time; and

ending the idle status; and

if the data control chip is not in the idle status, accessing to the PCI bus data stored in the debug card according to the settings of the data control chip;

storing the data in a buffer of the data control chip; and

extracting the data stored in the buffer of the data control chip via a host interface of the debug card,

wherein extracting the data stored in the buffer of the data control chip via a host interface of the debug card further comprising:

transferring the data stored in the buffer of the data control chip to the host; and

analyzing the data stored in the host,

wherein initializing the data control chip further comprising:

performing a synchronization setting of the data control chip and debug card;

setting an operating mode of the data control chip;

selecting a register address in the data control chip and writing an access control code therein;

setting a data access width of the data control chip; and

clearing the buffer of the debug card, and

wherein the determination of the data access situation of the debug card enables to evaluate whether the buffer of the debug card is full, which establishes a basis for calculating an accumulation of the data which means the amount of data accessed each time being accumulated into a data amount total, setting the amount of data determines a number of data packets to access it at a next non-idle status, and once the idle status ends, the control chip turns to the non-idle status to perform data accessing.

2-5.   (Cancelled)

6. (Previously Presented) The method of claim 1, wherein ending the idle status further comprising preparing to perform a next data access.

7. (Previously Presented) The method of claim 1, wherein if the data control chip is not in an idle status, accessing to the PCI bus data according to the settings of the data control chip means accessing to the PCI bus data according to a control code stored in a register of the data control chip.

8-13. (Cancelled)

14. (Previously Presented) The method of claim 1, wherein the data control chip is a chip of a model EZ-USBFX2.

15. (Cancelled)